

<b>Form 1449 (Modified)</b>	<b>Serial No.: 10/037,593</b>
	<b>Atty. Docket No.: GLVE0157</b>
<b>Information Disclosure</b>	<b>Applicants: Matthew P. KULIG, et al.</b>
<b>Statement By Applicant</b>	<b>Art Unit: 2457</b>
	<b>Confirmation No.: 6376</b>
<b>(Use Several Sheets if Necessary)</b>	<b>Filing Date: October 19, 2001</b>

## U.S. Patent Documents

Examiner Initials	No.	Patent No.	Issue Date	Patentee	Filing Date
	NJ	5,126,936	6/30/1992	Champion, et al.	
	NK	5,270,922	12/14/1993	Higgins	
	NL	6,058,391	5/2/2000	Gardner	
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	NN	6,931,408	8/16/2005	Adams, et al.	
	NO	7,305,383 B1	12/4/2007	Kubesh, et al.	
	NP	7,305,391 B2	12/4/2007	Wyschogrod, et al.	

## Published U.S. Patent Applications

Examiner Initials	No.	Publication No.	Publication Date	Applicant
	NQ	2001/0014093	8/16/2001	Yoda, et al.
	NR	2003/0018630	1/23/2003	Indeck et al.
	NS	2003/0033240	2/13/2003	Balson, et al.
	NT	2003/0037037	2/20/2003	Adams, et al.
	NU	2003/0055777	3/20/2003	Ginsberg
	NV	2008/0109413	5/8/2008	Indeck et al.

## Foreign Patent or Published Foreign Patent Applications

Examiner Initials	No.	Document No.	Publication Date	Applicant

## Non-Patent Literature Documents

Examiner Initials	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	NW	CONG, et al.; "An Optimal Technology Mapping Algorithm for Delay Optimization in Lookup-Table Based FPGA Designs"; 1992; IEEE, pp. 48-53.

/Barbara Burgess/

Examiner's Signature: \_\_\_\_\_

11/06/2009

Date: \_\_\_\_\_

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.